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EFS ID:	3772677				
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International Application Number:					
Confirmation Number:	7583				
Title of Invention:	SEMICONDUCTOR DEVICE INCLUDING BAND-ENGINEERED SUPERLATTICE				
First Named Inventor/Applicant Name:	Robert J. Mears				
Correspondence Address:	Christopher F. Regan, Allen, Dyer, Doppelt, Milbrath, Gilchrist, P.A. P. O. Box 3791 - Orlando FL 32802-3791 US 4078412330 -				
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Document Number	Document Description	File Name	File Size(Bytes) /Message Digest	Multi Part /.zip	Pages (if appl.)		
1	1 Miscellaneous Incoming Letter	62601_CON1_LOSS_SMAL L_ENTITY_ENTITLEMENT. pdf	25978	no	1		
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If a new application is being filed and the application includes the necessary components for a filing date (see 37 CFR 1.53(b)-(d) and MPEP 506), a Filing Receipt (37 CFR 1.54) will be issued in due course and the date shown on this Acknowledgement Receipt will establish the filing date of the application.

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If a timely submission to enter the national stage of an international application is compliant with the conditions of 35 U.S.C. 371 and other applicable requirements a Form PCT/DO/EO/903 indicating acceptance of the application as a national stage submission under 35 U.S.C. 371 will be issued in addition to the Filing Receipt, in due course.

New International Application Filed with the USPTO as a Receiving Office

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